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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant:

Kiyoshi Arita

Serial No.:

10/716,965

Art Unit: 2813

Filing Date:

November 19, 2003

Title:

"METHOD OF MANUFACTURING SEMICONDUCTOR DEVICE PLASMA PROCESSING APPARATUS AND

PLASMA PROCESSING METHOD"

Docket No.:

36280

SUPPLEMENTAL INFORMATION DISCLOSURE STATEMENT

Commissioner for Patents P.O. Box 1450 Alexandria, Va. 22313-1450

Sir/Madam:

In accordance with Rule 56, applicant is aware of the publications listed in the International Search Report (copy enclosed) and in the enclosed copy of Patent Office Form 1449. A copy of each of the publications is enclosed herewith. The undersigned hereby certifies that each item of information contained in this Supplemental Information Disclosure Statement was first cited in any communication from a foreign patent office in a counterpart foreign application not more than three months prior to the filing of this Supplemental Information Disclosure Statement.

If there are any fees resulting from this communication, please charge such fees to our Deposit Account No. 16-0820, Order No. 36280.

Respectfully submitted,

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May 12, 2004

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents, P.O. Box 1450, Alexandria, Va. 22313-1450 on the date indicated below.

Jeffrey J. Sopko

Name of Actorney for Applicant(s)

05/12/2004

Date

//Signature of Attorney

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U.S. DEPARTMENT OF COMMERCE PATENT AND TRADEMARK OFFICE

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SERIAL NO. 10/716,965

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(USE SEVERAL SHEETS IF NECESSARY)

APPLICANT: Kiyoshi Arita

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GROUP ART UNIT: 2813

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